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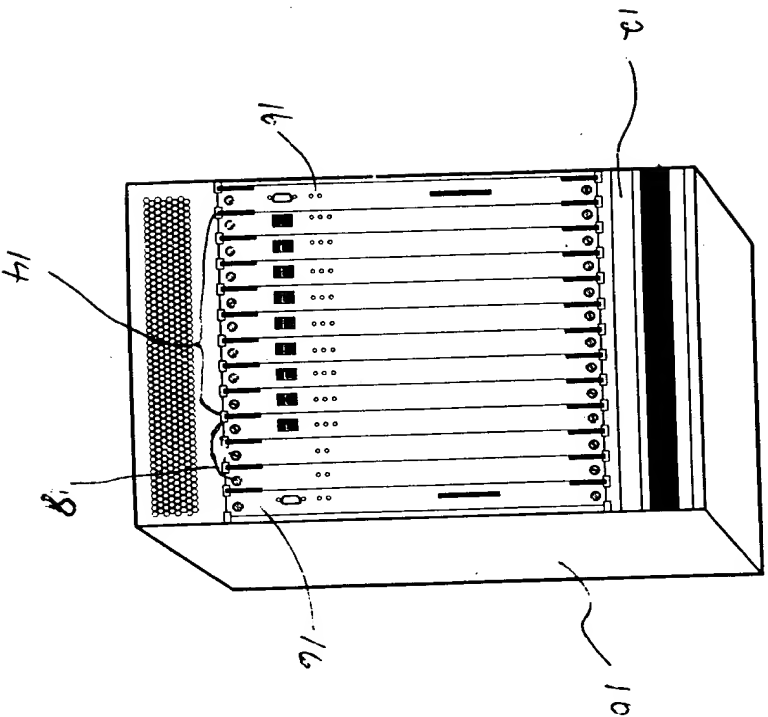


FIG. 1 is a perspective view of a server rack (10) containing multiple server units (12). The units are connected to a network switch (14) via cables (16). The rack is shown from a three-quarter view, highlighting the front and side panels. The server units are arranged vertically, and the network switch is located at the bottom of the rack. Cables (16) are shown connecting the server units to the network switch (14).

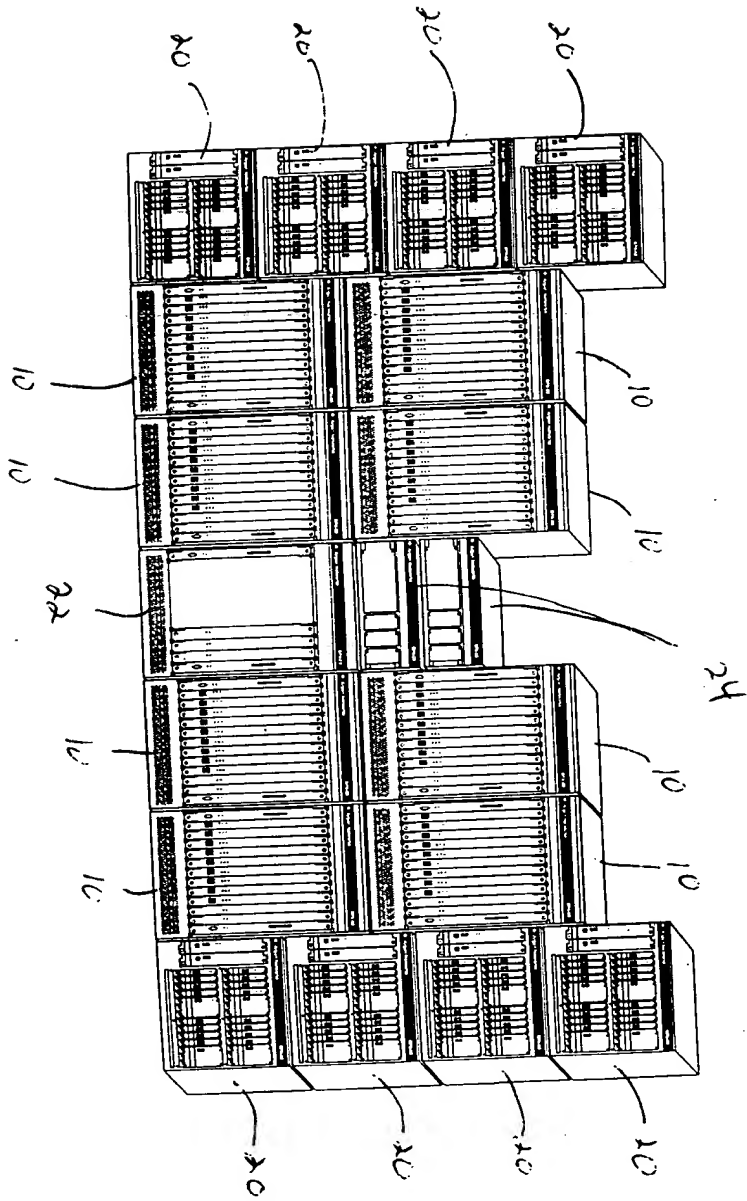


FIG. 1 is a perspective view of the device of the present invention.

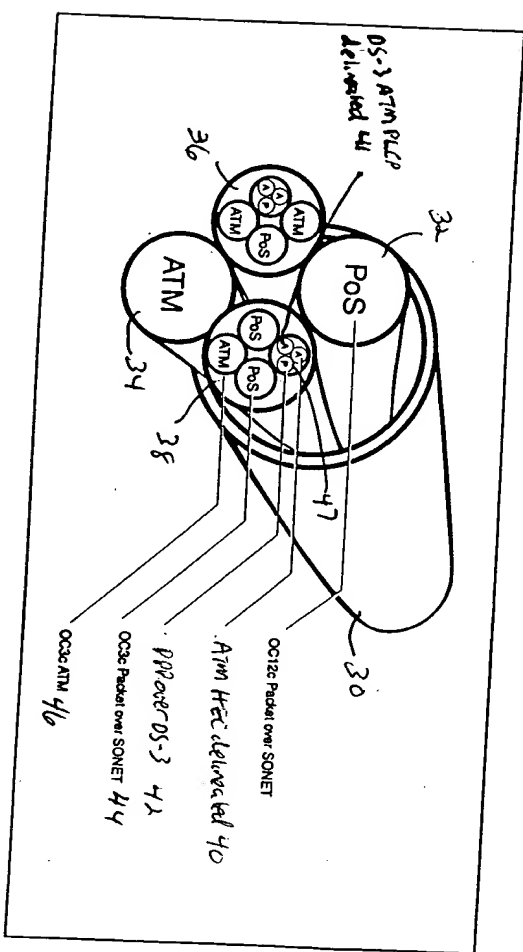


Figure 3

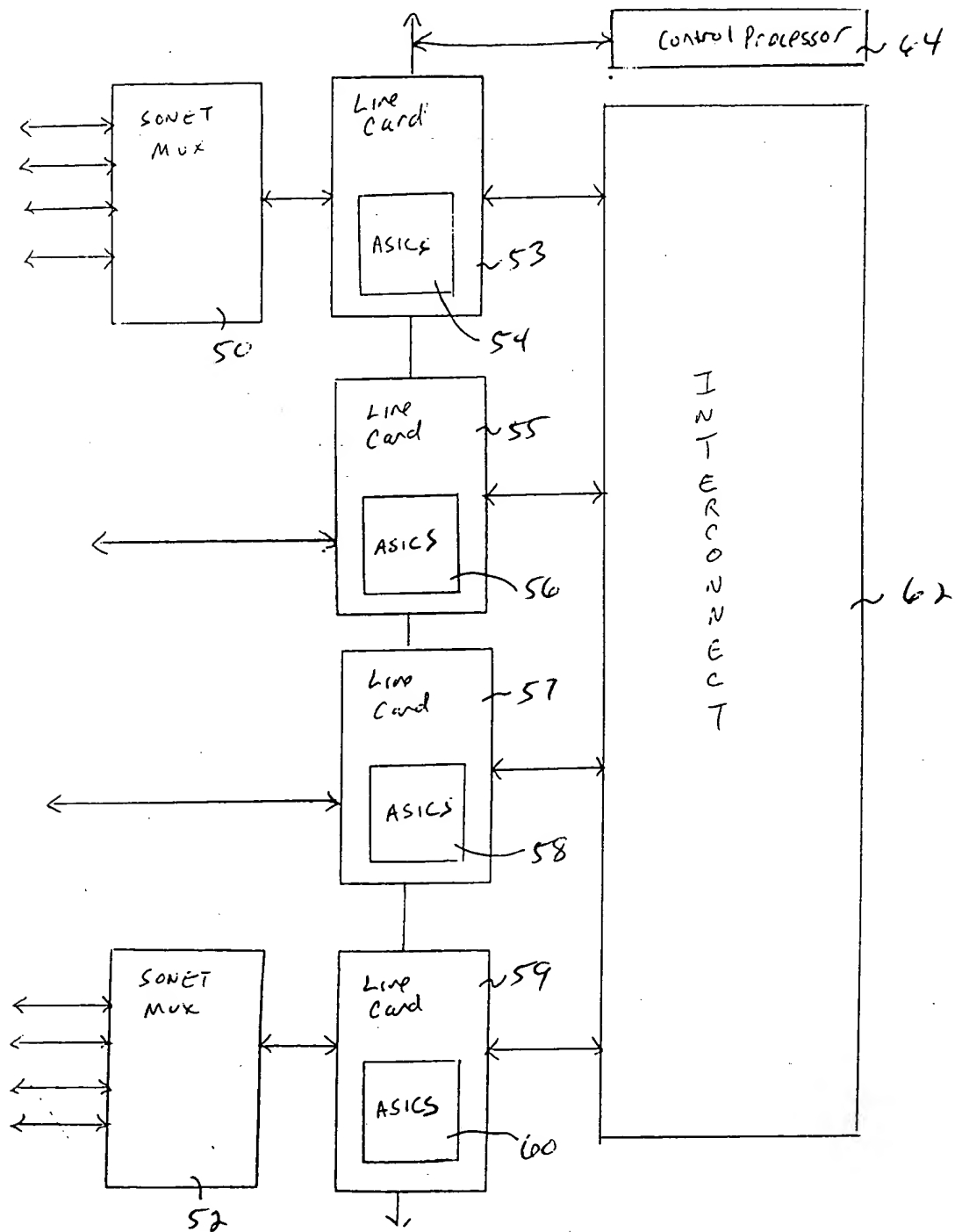


Figure 4

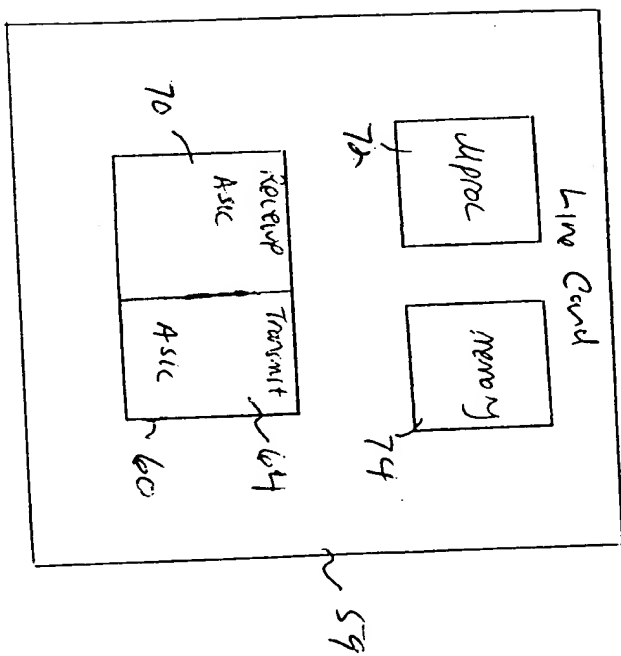
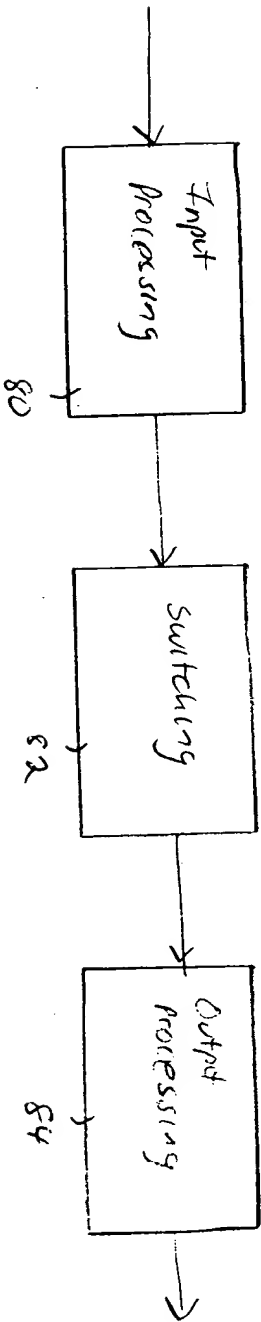


Figure 5



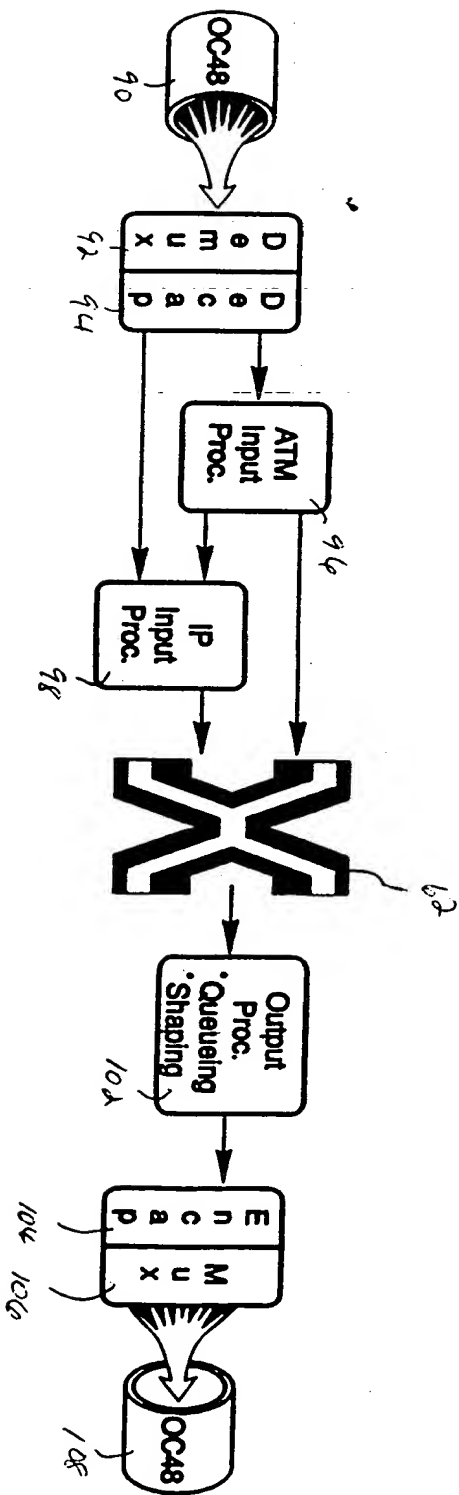


Figure 7

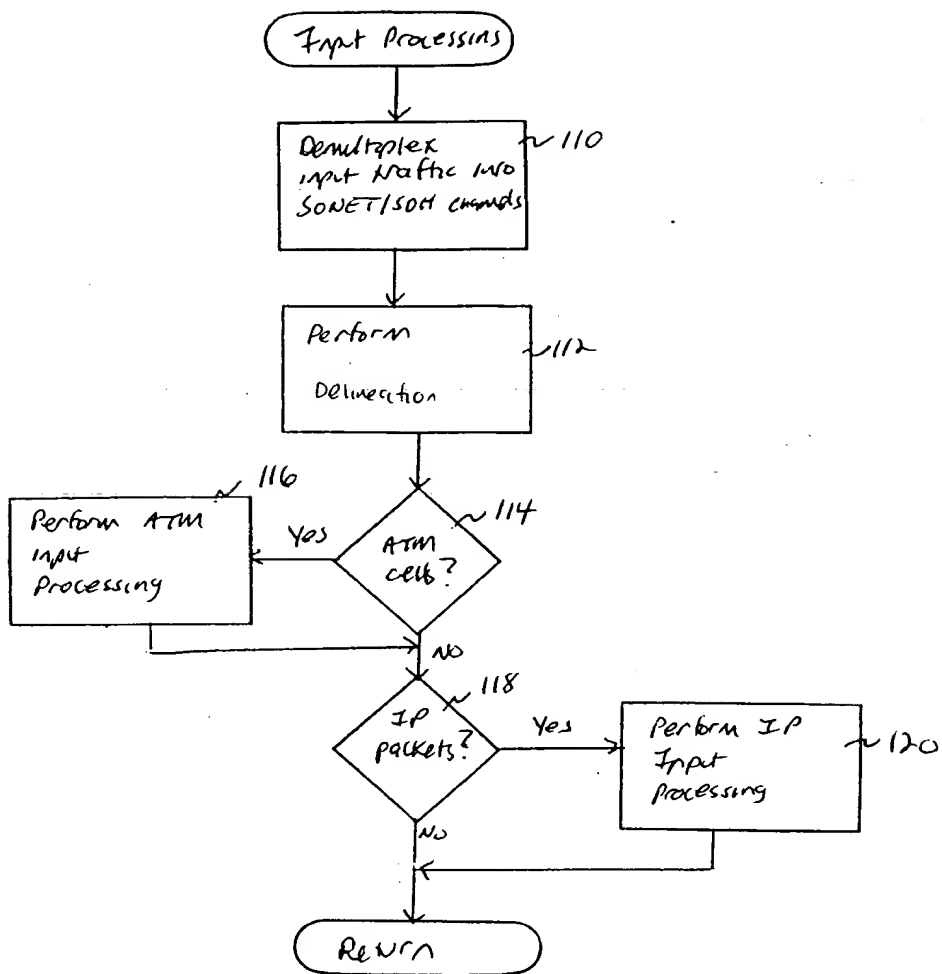


Figure 8



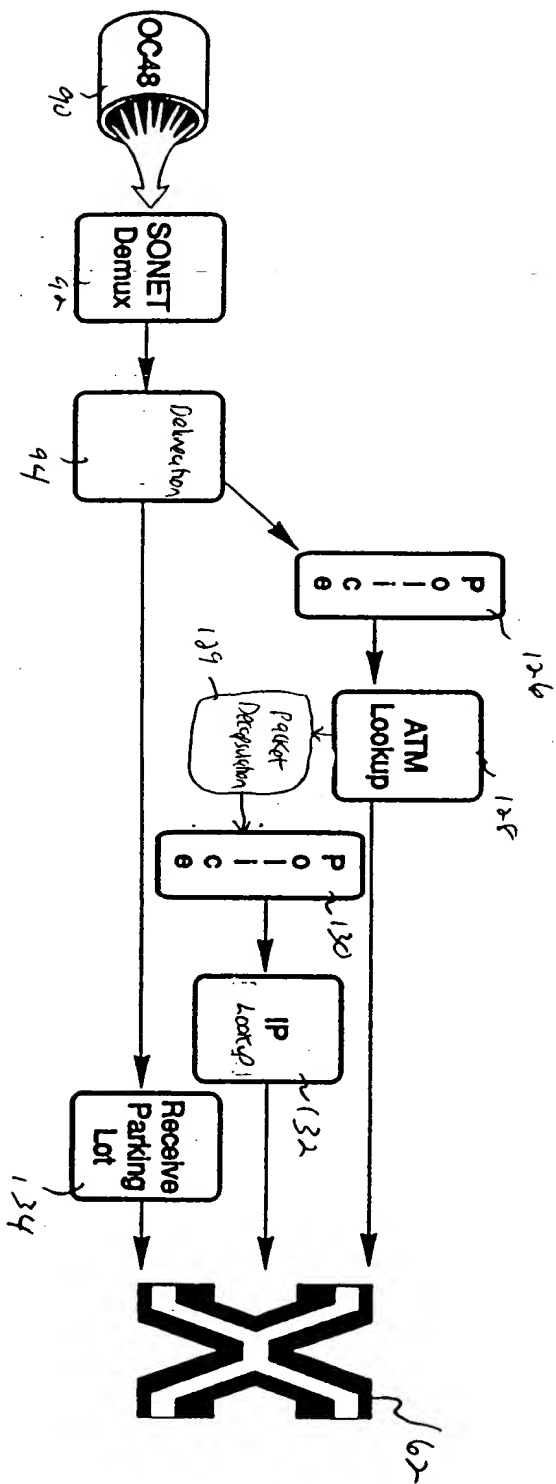


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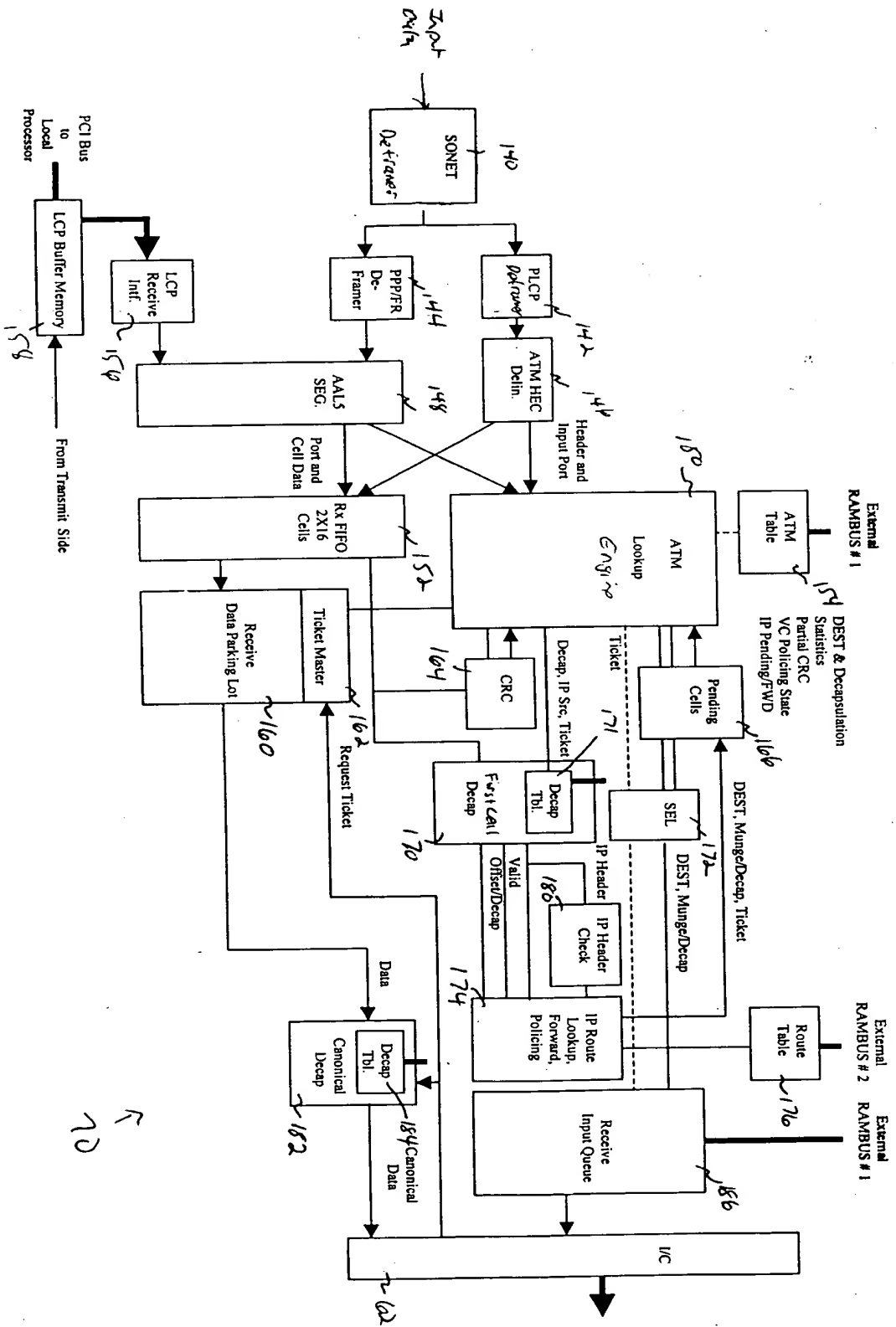


Figure 10

FIG. 10 is a block diagram of a network processor architecture.

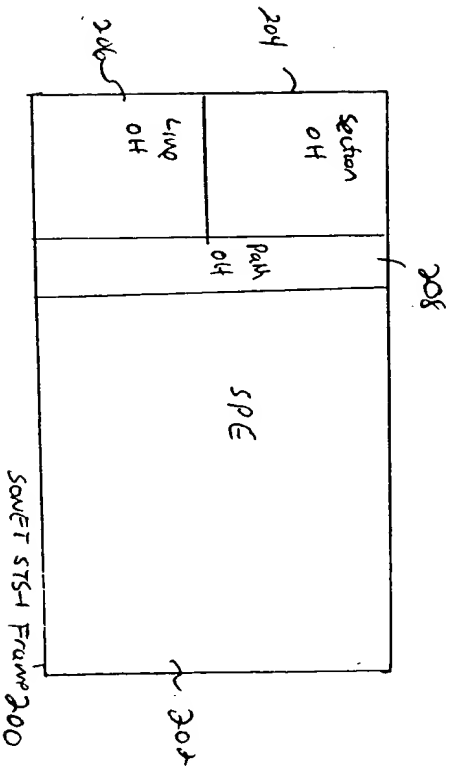


Figure 11

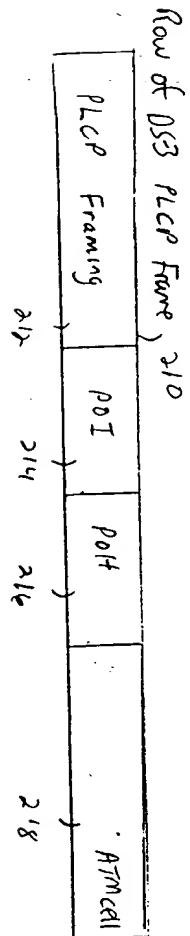


Figure 12

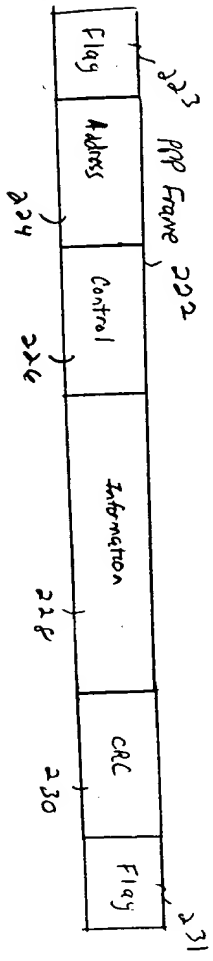
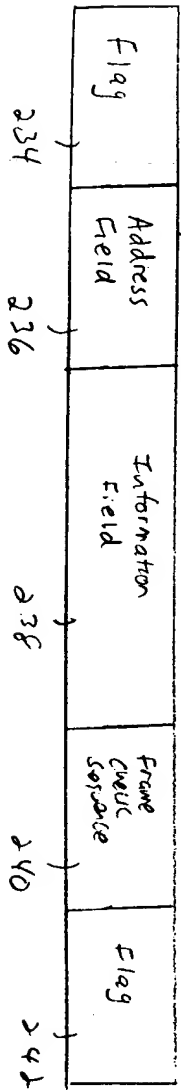


Figure 13

FIG. 13 is a block diagram of an RPP frame structure. The frame is divided into six fields: Flag, Address, Control, Information, CRC, and Flag. The bit positions for each field are indicated by brackets and labels: the first Flag field spans from bit 223 to 224; the Address field spans from bit 224 to 226; the Control field spans from bit 226 to 228; the Information field spans from bit 228 to 230; the CRC field spans from bit 230 to 231; and the final Flag field spans from bit 231 to 232. The entire frame is labeled 'RPP Frame 222'.

Frame Relay frame 232



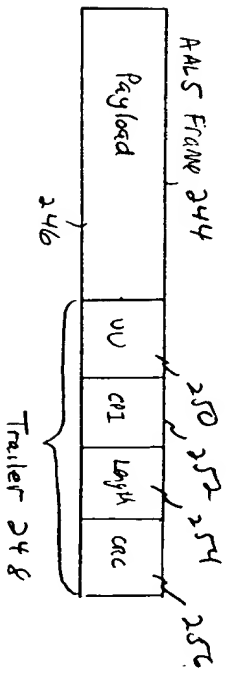


Figure 15

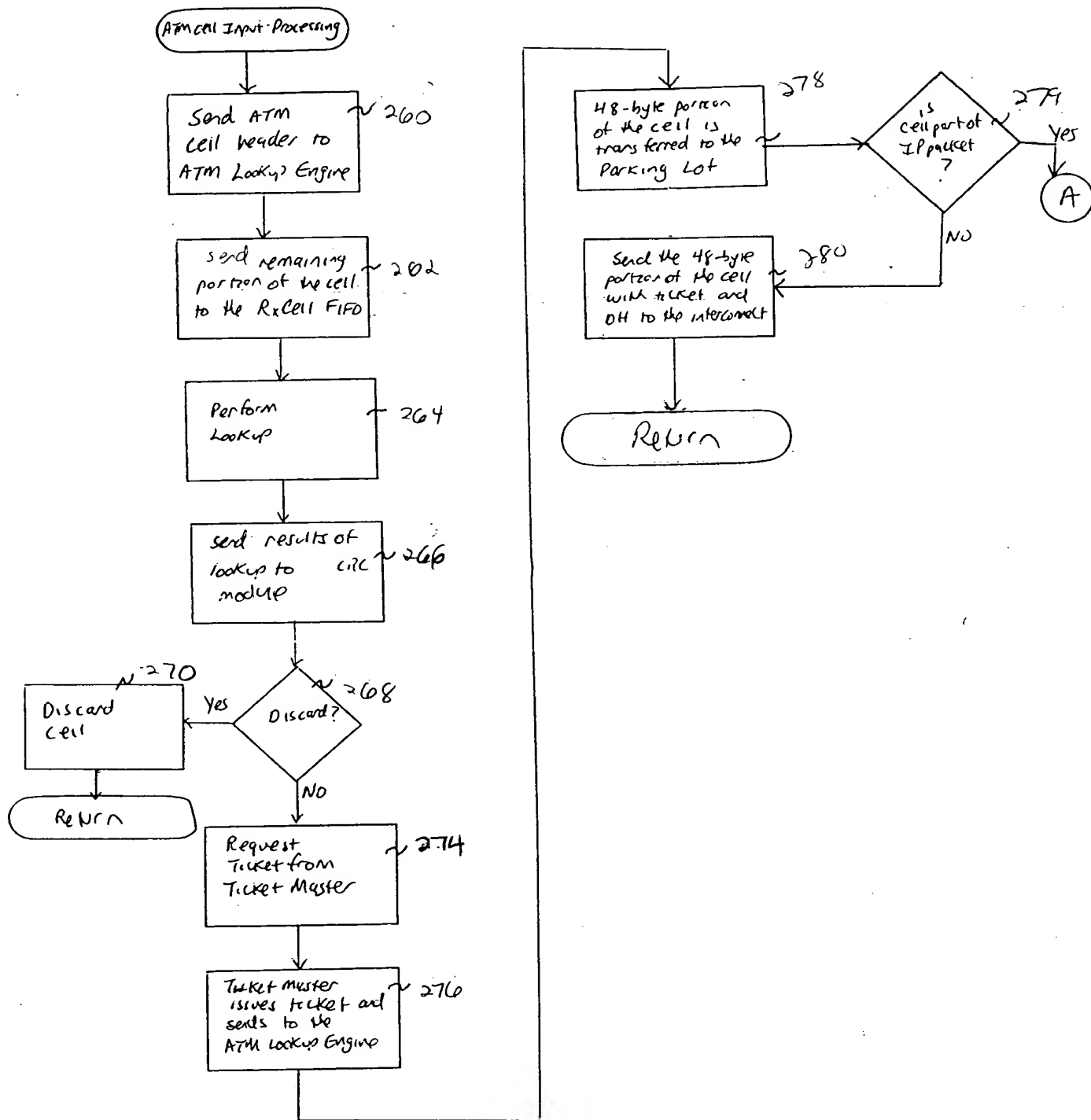


Figure 16



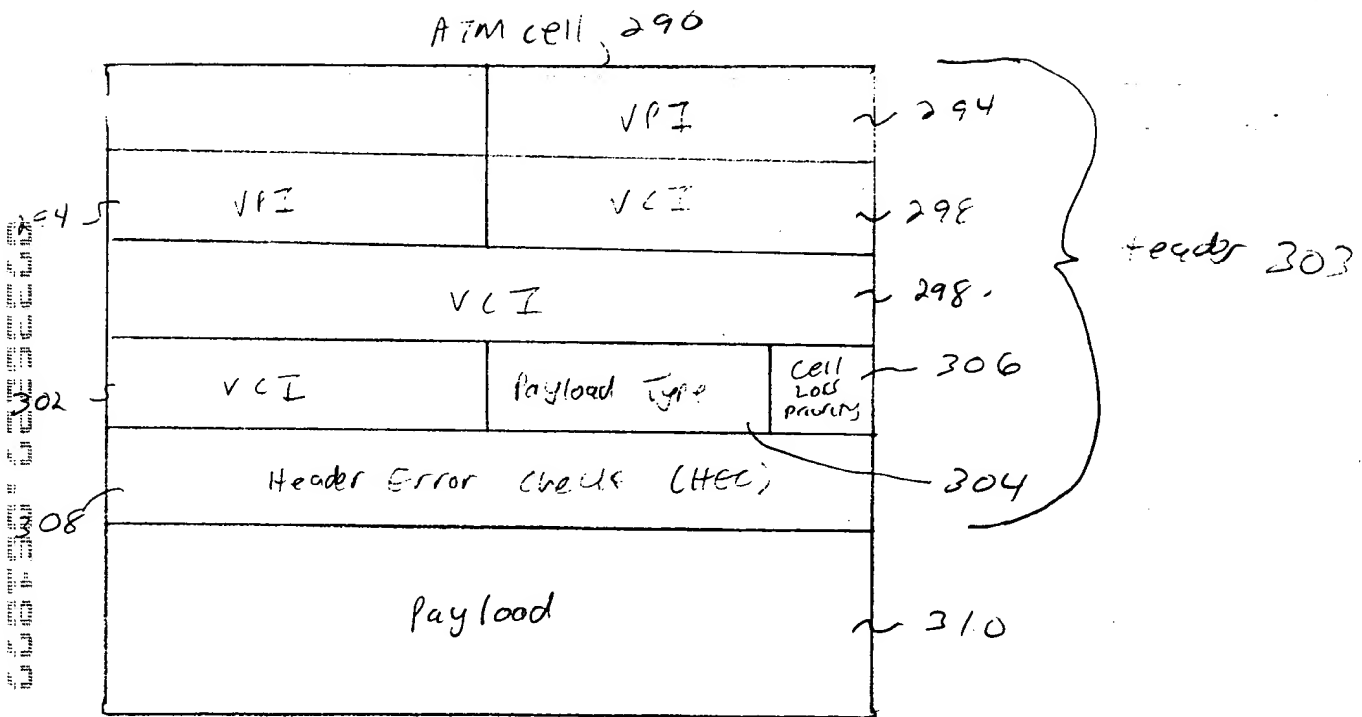


Figure 17

312 →

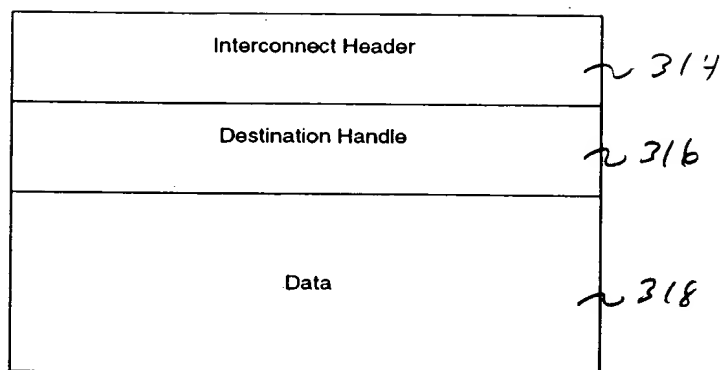


Figure 18

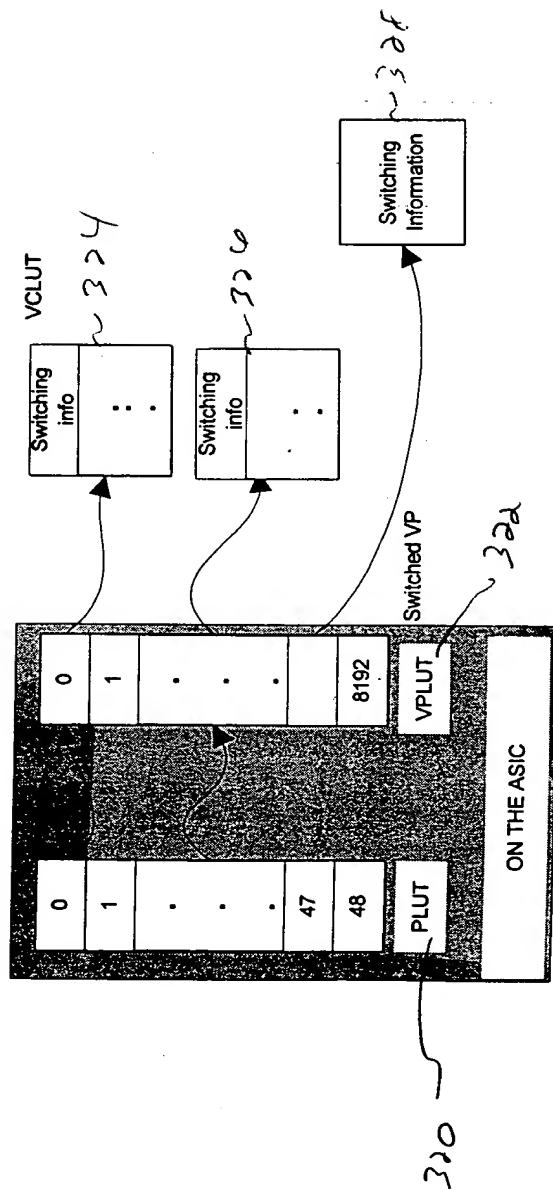
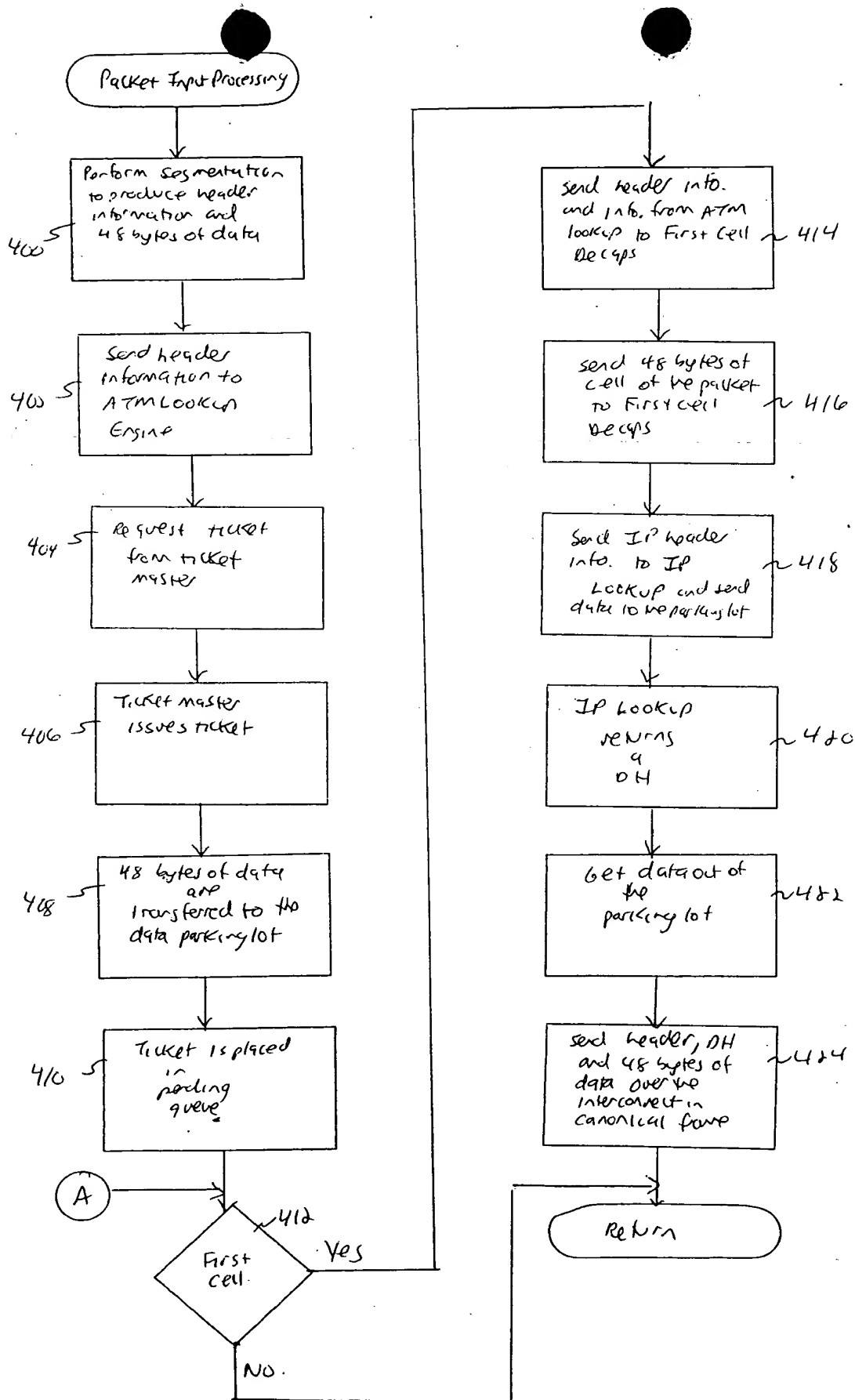


Figure 19



Figure

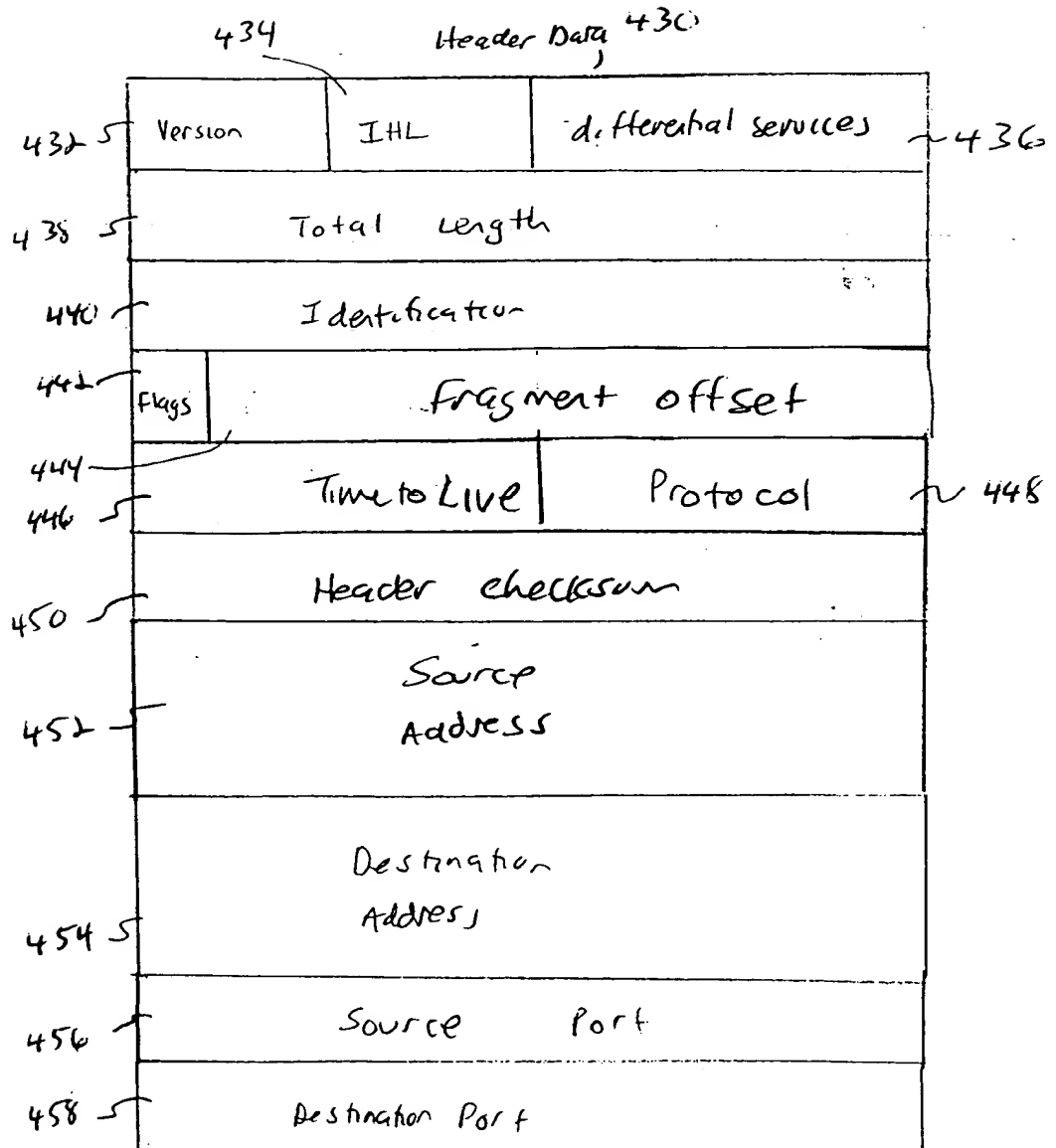


Figure 21

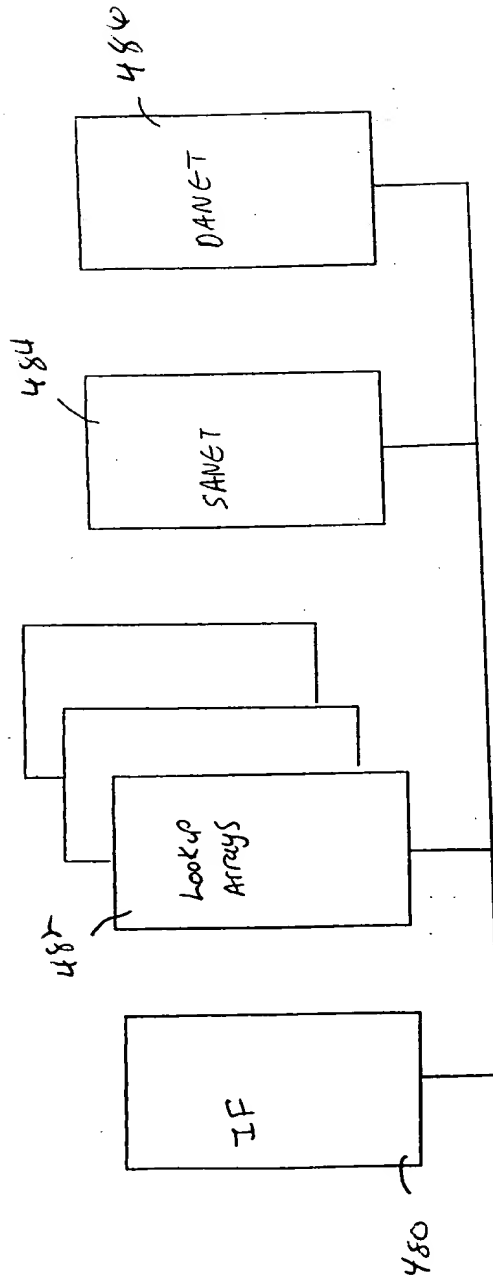


Figure 22

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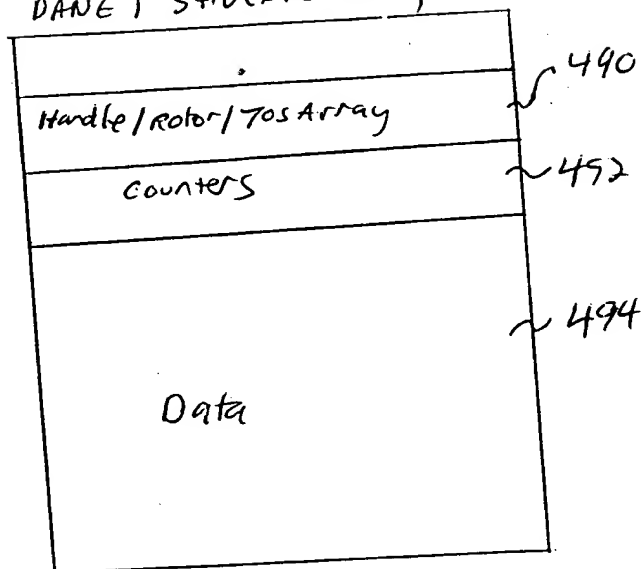


Figure 23

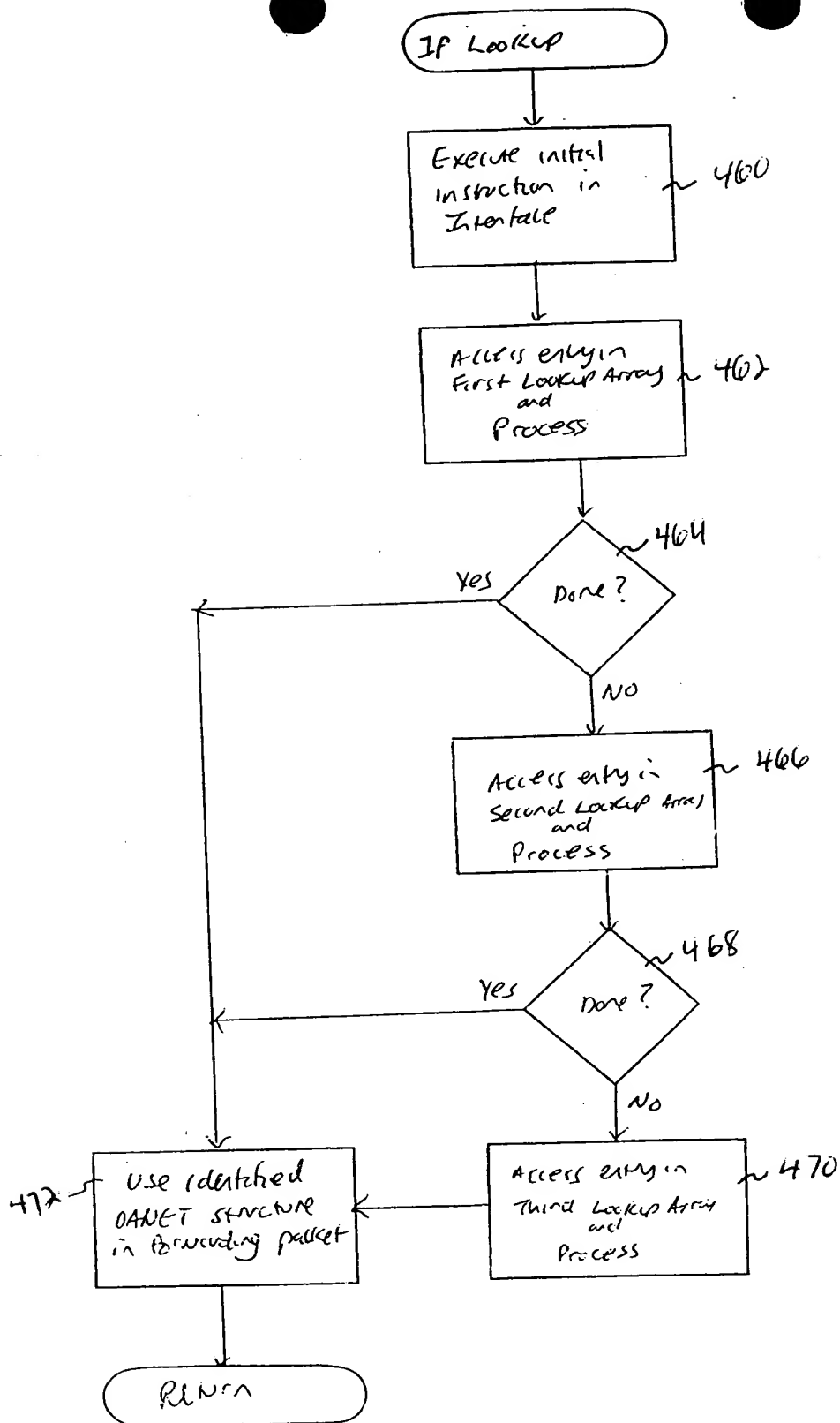
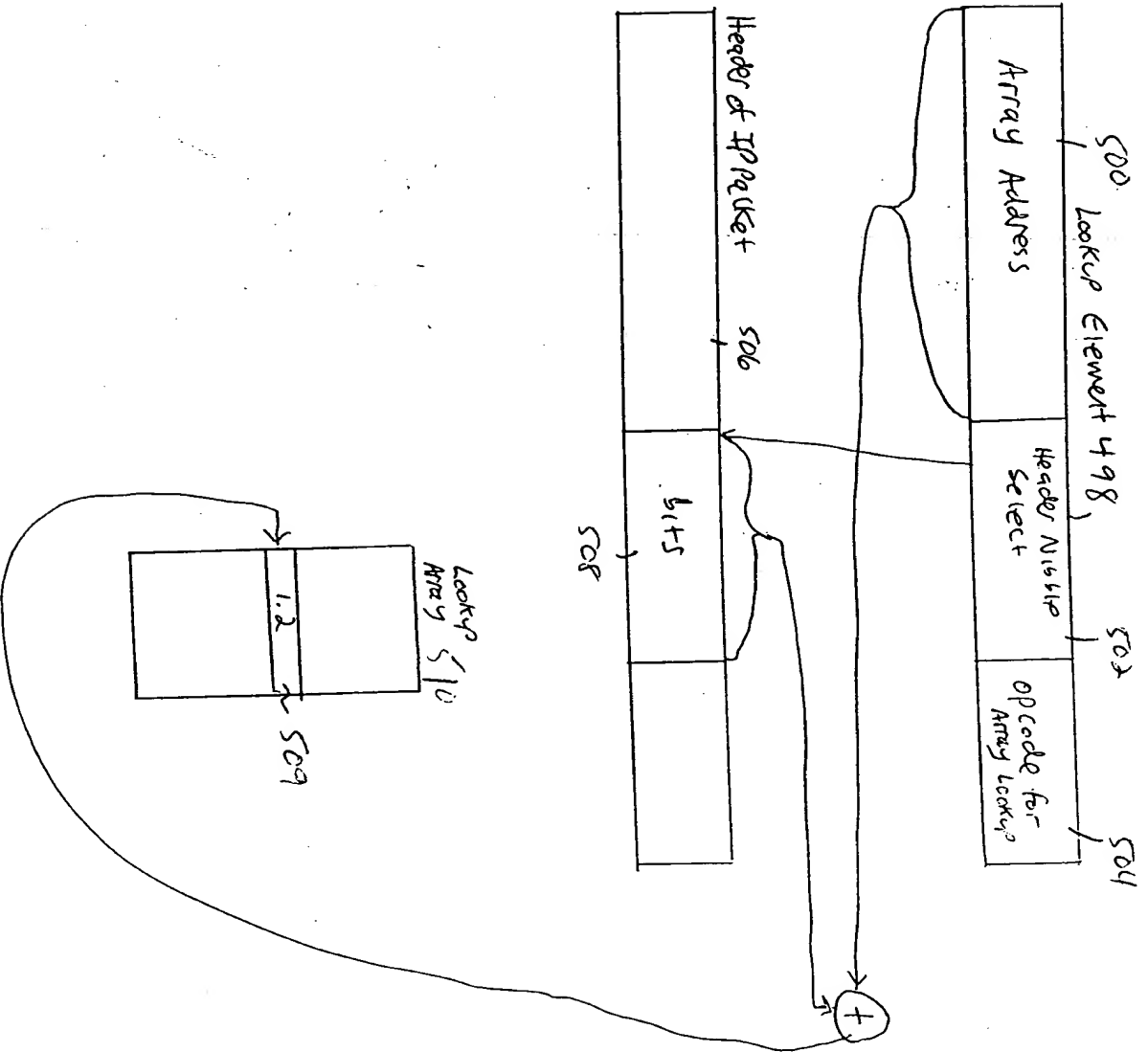
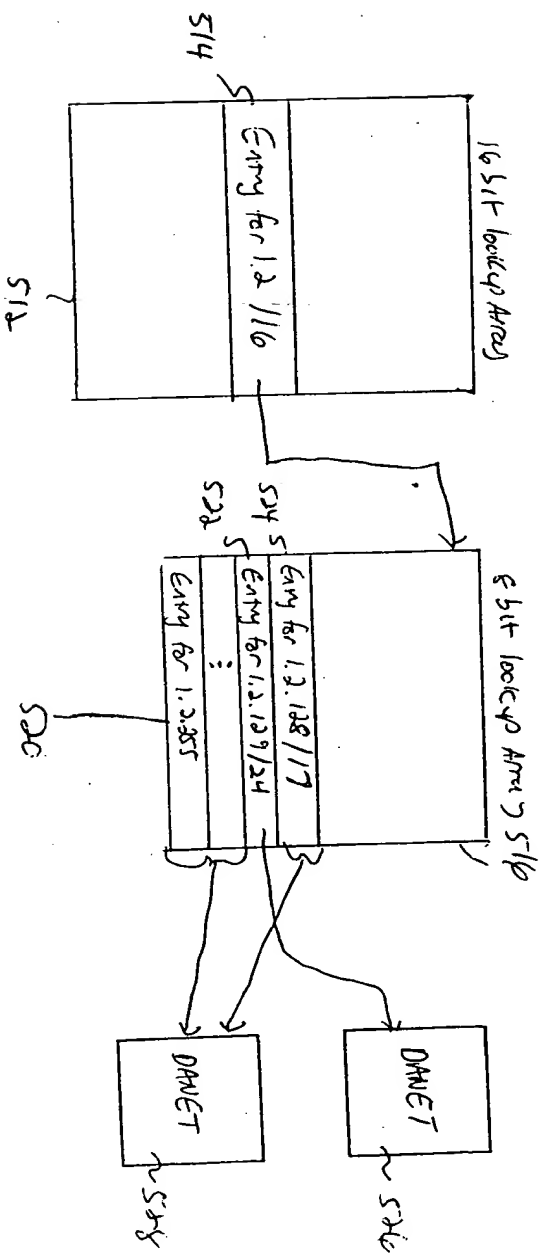


Figure 24







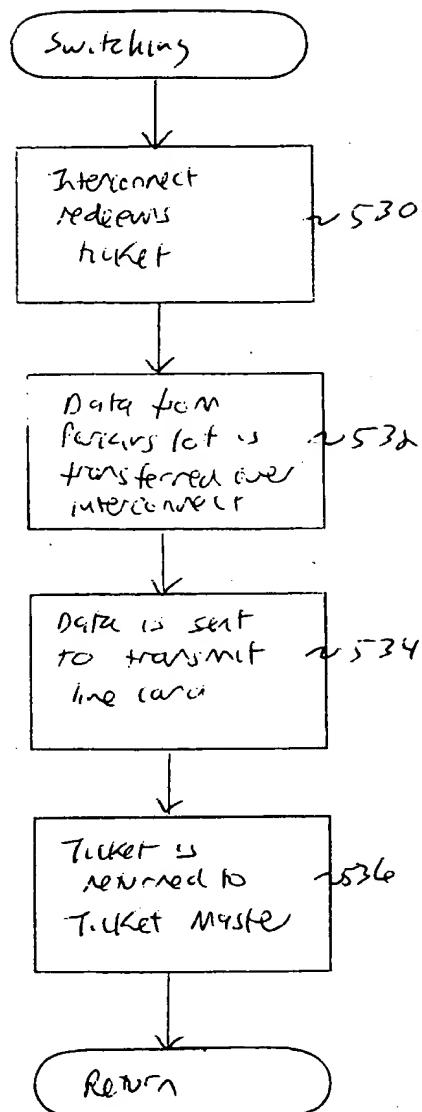


Figure 37

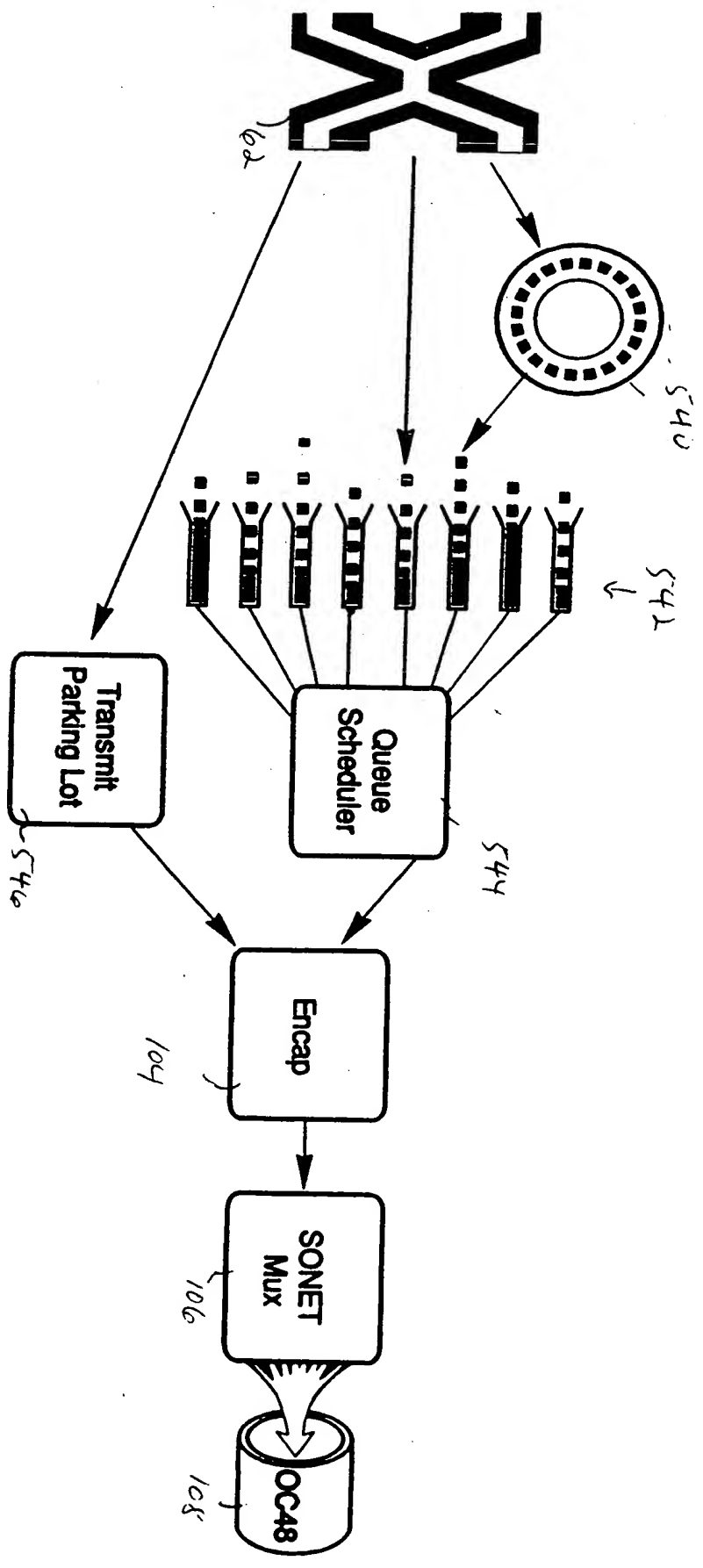
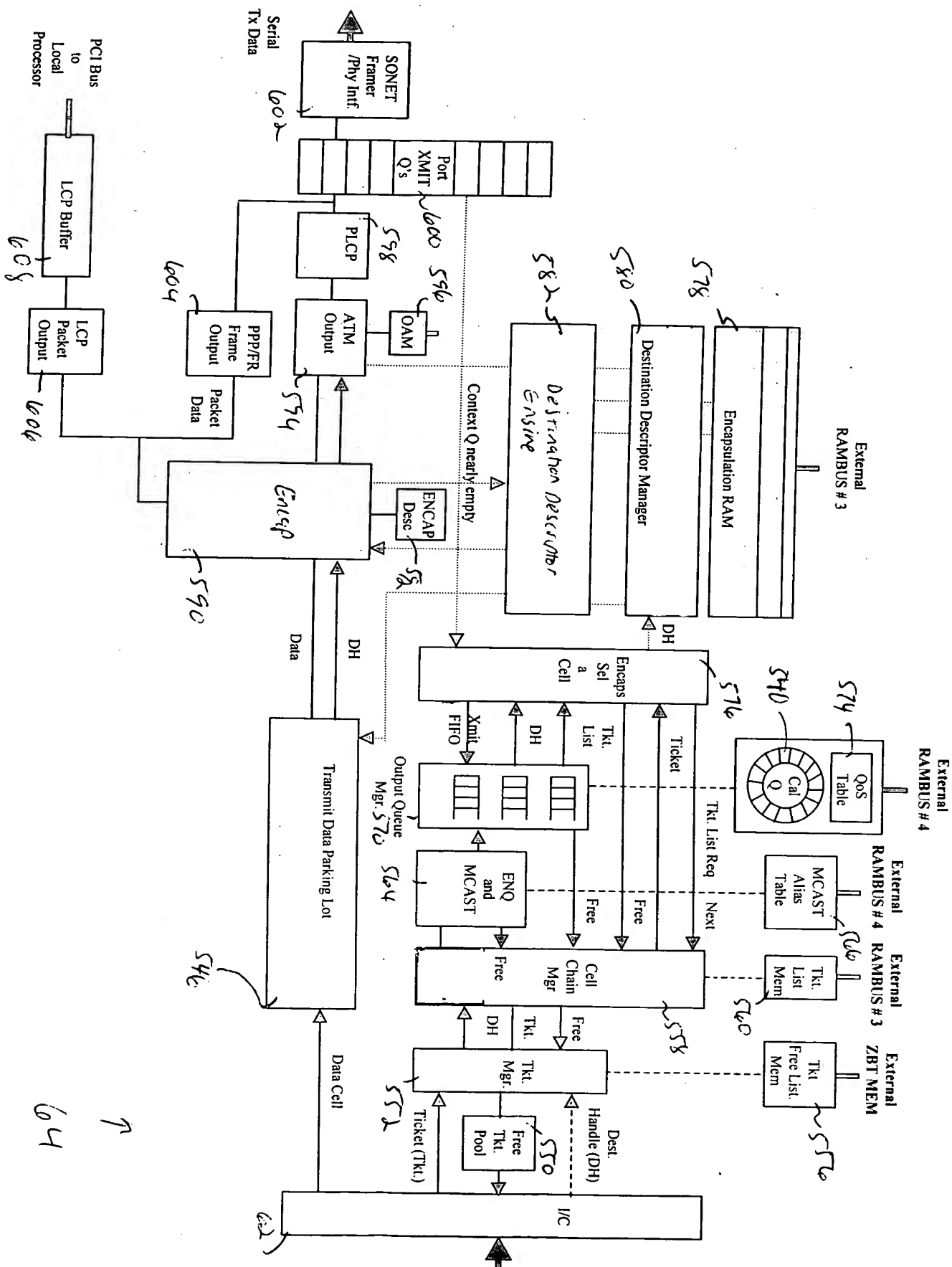
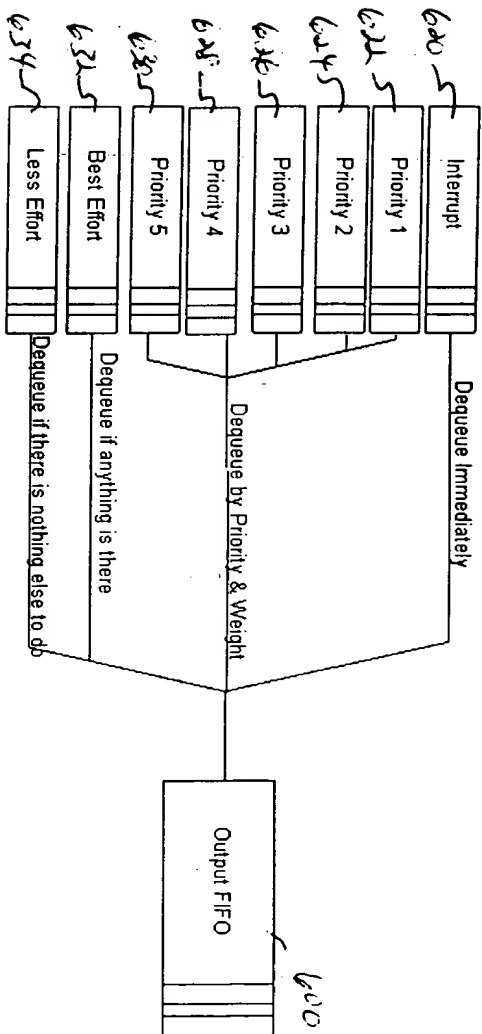
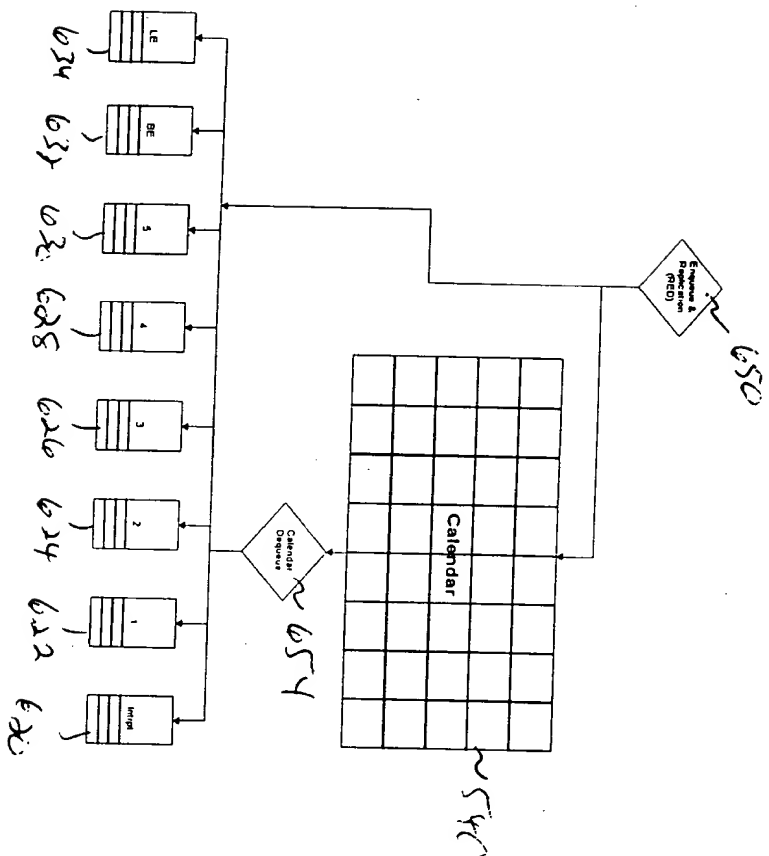


FIG. 1 is a block diagram of a network architecture.





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